

CLAIMS

What is claimed is:

- 5 1. A system for changing a bus configuration of a computing device,
said system comprising:
- a first bus of said computing device;
 - a second bus;
 - a third bus;
 - 10 a multiplexing module coupled with said first bus, said second bus, and
said third bus and for selectively coupling said first bus with said second bus or
said third bus; and
 - a configuration module coupled with said multiplexing module and for
controlling operation of said multiplexing module.
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2. The system of Claim 1, further comprising:
- a first controller adapter chip coupled with said second bus; and
 - a second controller adapter chip coupled with said third bus.
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3. The system of Claim 2, further comprising:
- a first add-in card slot coupled with said first controller adapter chip; and
 - a second add-in card slot coupled with said second controller adapter
chip.

4. The system of Claim 1, further comprising:
a first add-in card slot coupled with said second bus; and
a second add-in card slot coupled with said third bus.

5 5. The system of Claim 1, wherein said multiplexing module
comprises electrical circuitry.

6. The system of Claim 1, wherein said multiplexing module
comprises:

- 10 a first configuration routing for coupling said first bus with said second
bus; and
a second configuration routing for coupling said first bus with said third
bus.

15 7. The system of Claim 1, wherein said configuration module
comprises a register.

8. The system of Claim 1, wherein said configuration module
comprises a switch.

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9. A method for changing a bus configuration of a computing device,
said method comprising:

transmitting a first control signal to a configuration module; and

causing a multiplexing module to couple a first bus with a second bus of

25 said computing device, in response to said first control signal.

10. The method as described in Claim 9, further comprising:
transmitting a second control signal to said configuration module; and
causing said multiplexing module to couple said first bus with a third bus
5 in response to said second control signal.

11. The method as described in Claim 9, further comprising:
causing a controller adapter chip coupled with said second bus to go off-
line.

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12. The method as described in Claim 11, further comprising:
causing an add-in card to enter a sleep mode, said add-in card coupled
with an add-in card slot which is coupled with said controller adapter chip.

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13. The method as described in Claim 12, further comprising:
activating said controller adapter chip and said add-in card once said first
bus is coupled with said second bus.

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14. The method as described in Claim 9, further comprising:
causing an add-in card to enter a sleep mode, said add-in card coupled
with an add-in card slot which is coupled with said second bus.

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15. A system for modifying an input/output (I/O) bus configuration of a
computer system, said system comprising:
a first I/O bus of said computer system;

a second I/O bus;

a third I/O bus;

a multiplexer circuitry coupled with said first I/O bus, said second I/O bus,
and said third I/O bus and for selectively coupling said first I/O bus with said

5 second I/O bus or said third I/O bus; and

a configuration module coupled with said multiplexer circuitry and for
controlling operation of said multiplexer circuitry.

16. The system of Claim 15, further comprising:

10 a first controller adapter chip coupled with said second I/O bus; and
a second controller adapter chip coupled with said third I/O bus.

17. The system of Claim 16, further comprising:

a first add-in card slot coupled with said first controller adapter chip; and
15 a second add-in card slot coupled with said second controller adapter
chip.

18. The system of Claim 15, further comprising:

a first add-in card slot coupled with said second I/O bus; and
20 a second add-in card slot coupled with said third I/O bus.

19. The system of Claim 15, wherein said multiplexer circuitry
comprises:

a first configuration routing for coupling said first I/O bus with said second
25 I/O bus; and

a second configuration routing for coupling said first I/O bus with said third I/O bus.

20. The system of Claim 15, wherein said configuration module
5 comprises a register.

21. The system of Claim 15, wherein said configuration module
comprises a hardware switch.